

GP104 - 8GB GDDR5, 256b, 256Mx32  
Tall DVI-D + DP + DP + DP/HDMI + DP

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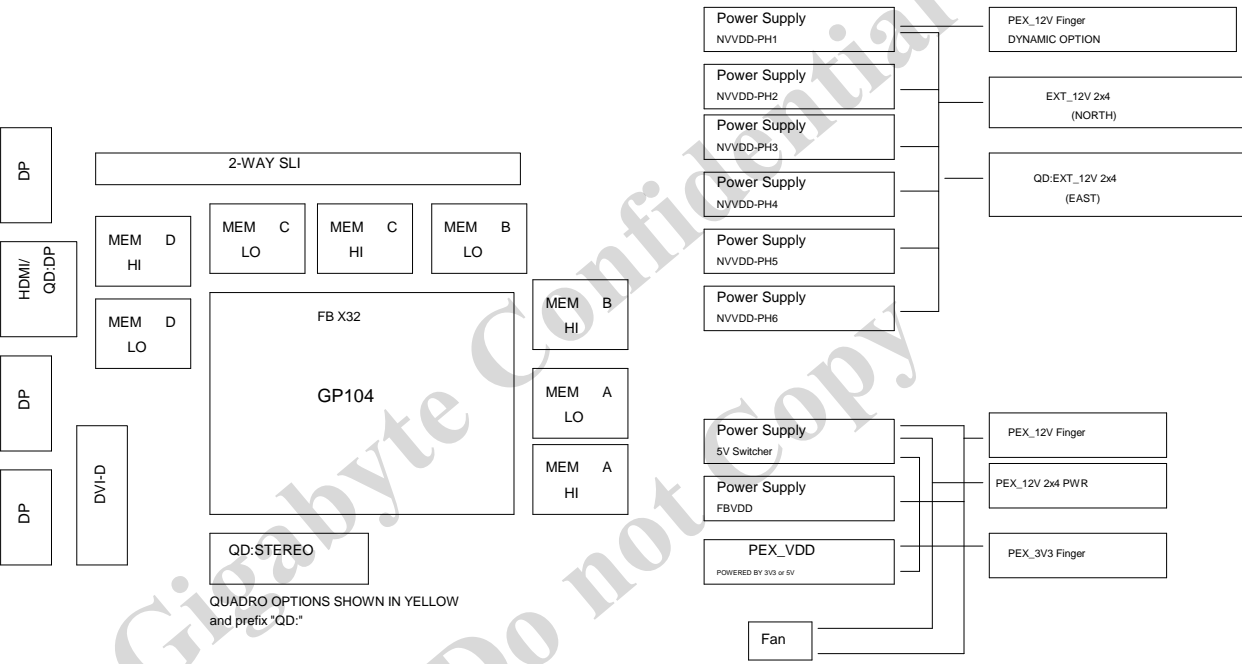
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ASSEMBLY  
PAGE DETAIL

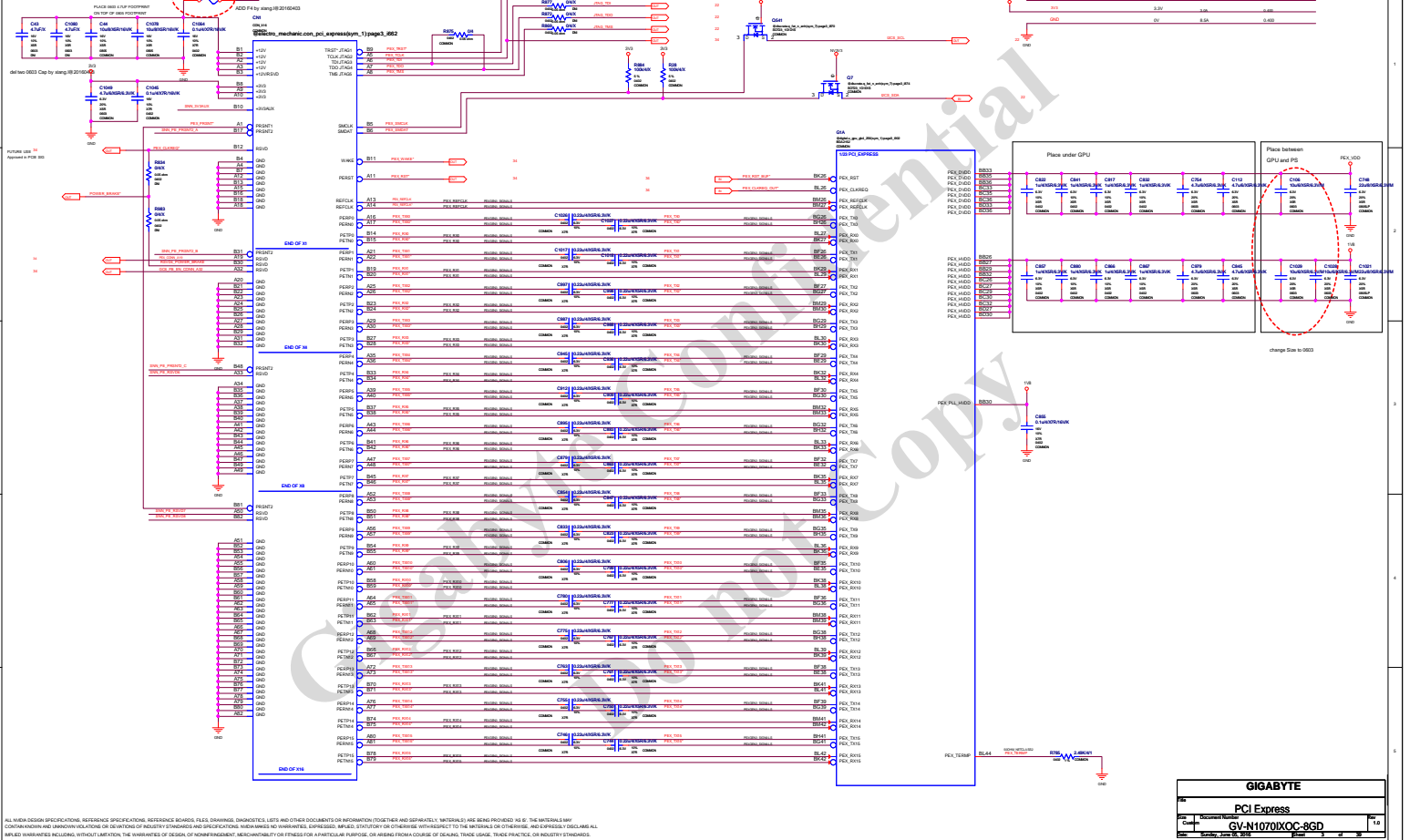
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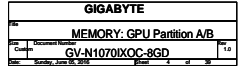
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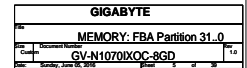
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Block Diagram	
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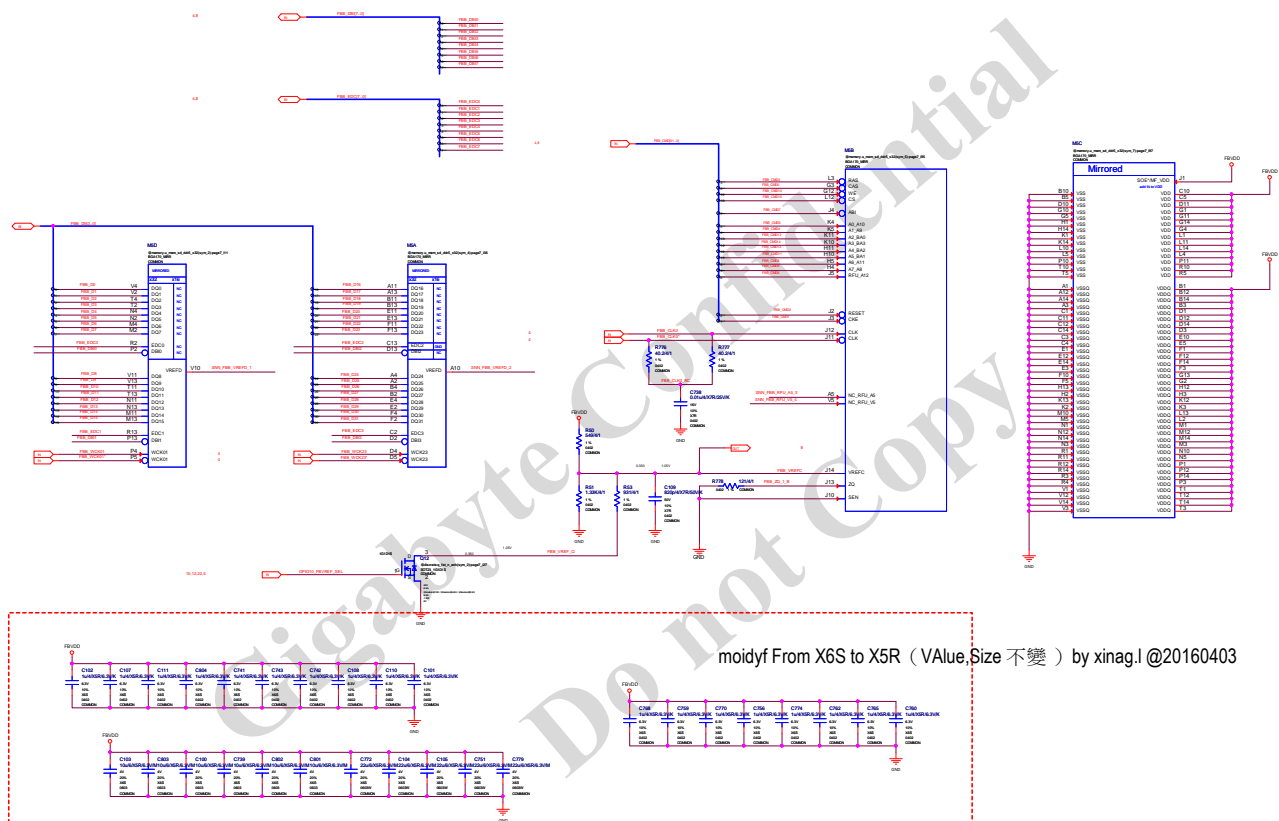
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PCI Express	
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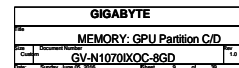
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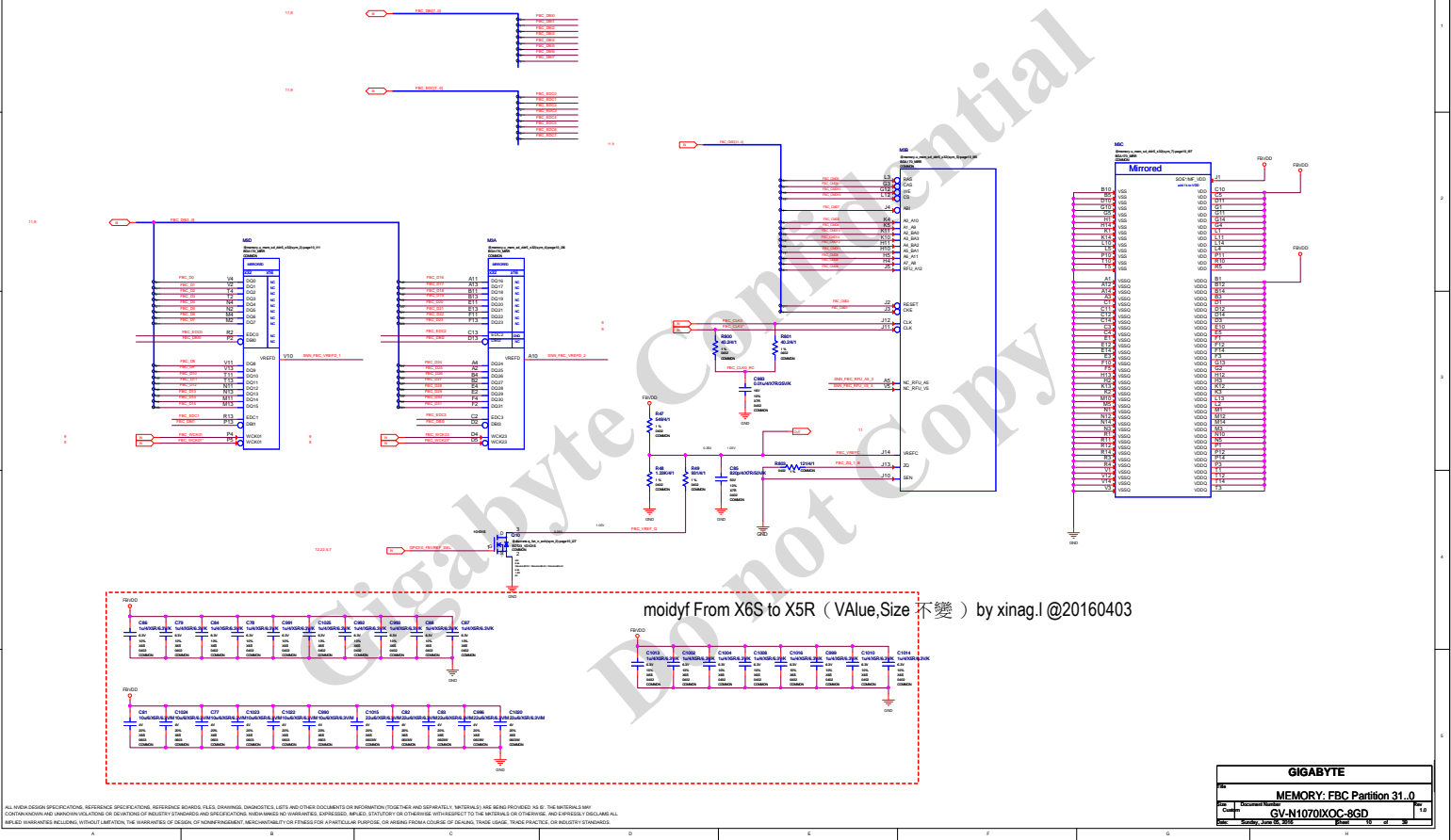
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MEMORY: FBB Partition 31..0	
GV-N1070IXOC-8GD	

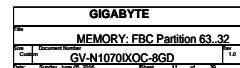






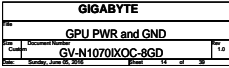


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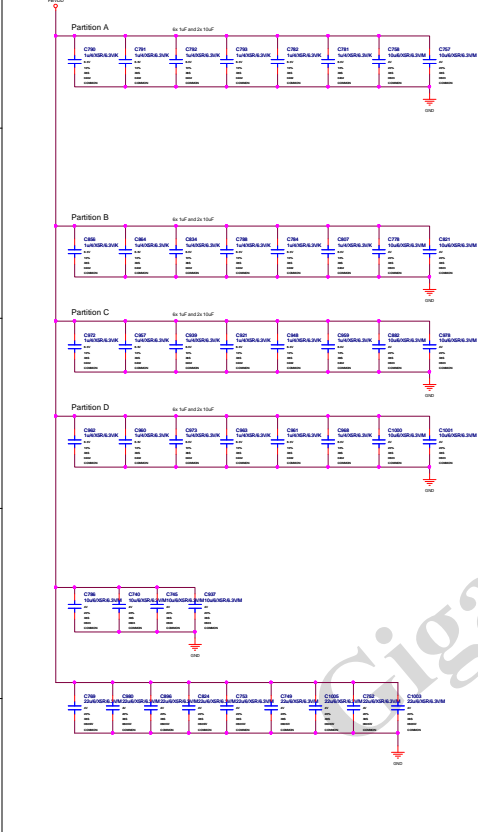




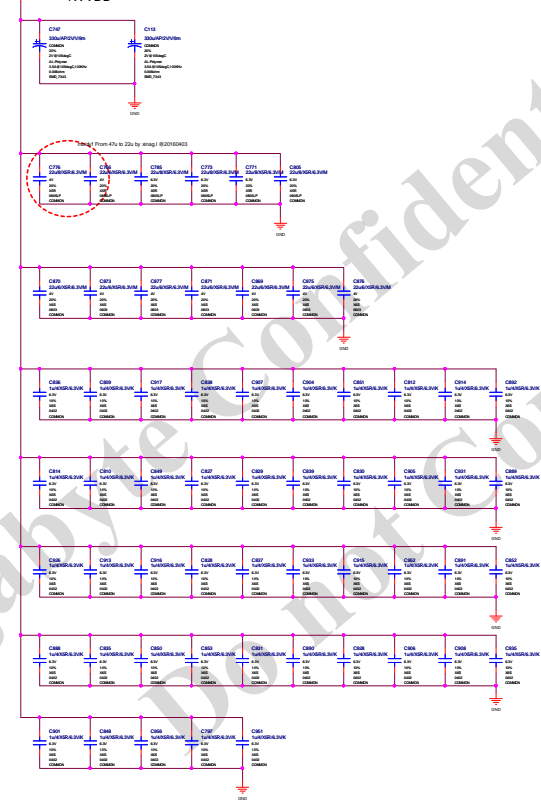




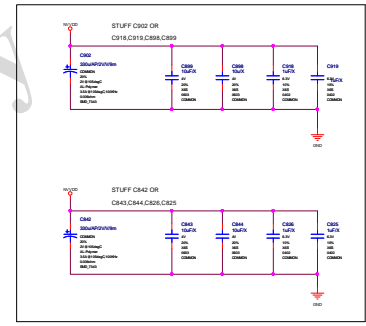
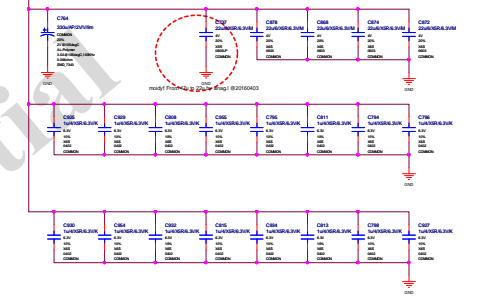
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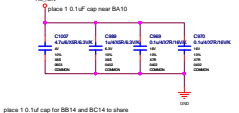
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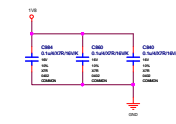
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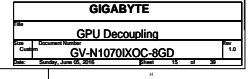
1V8\_AON

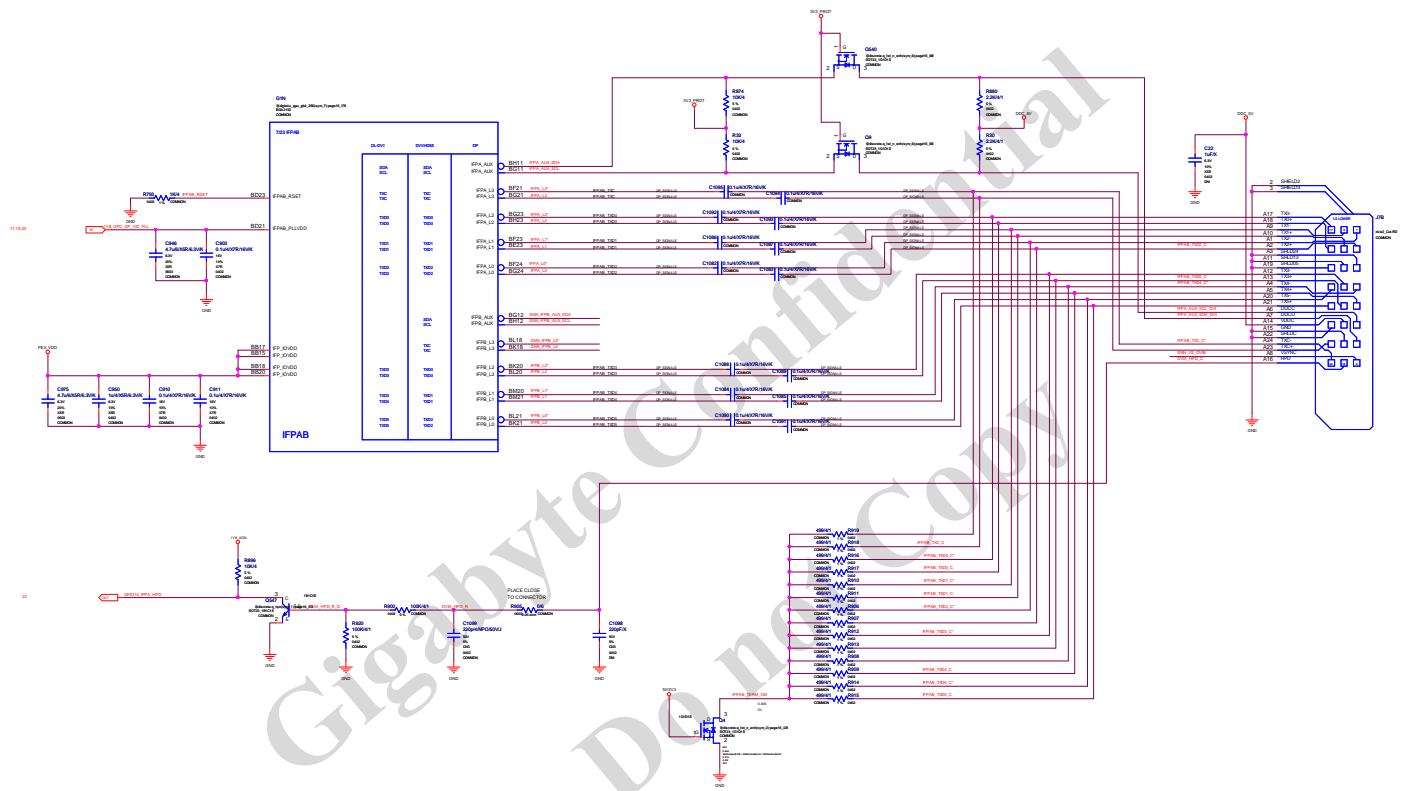


VDD18



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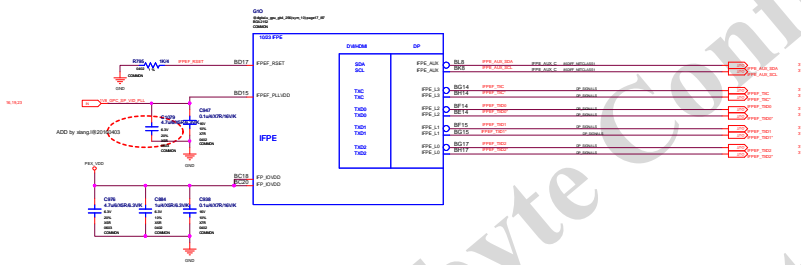
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GIGABYTE	
IFPAB DVI-D-DL	
GV-N1070XOC-8GD	



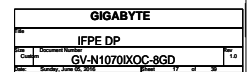
Two cases to be considered:  
1. DP AUX to DP connector: AUX AC coupled  
2. DP AUX to DP DVI dongle: AUX pass through

DEL DP J3 by xiang.l@20160402



Fused DP\_PWR

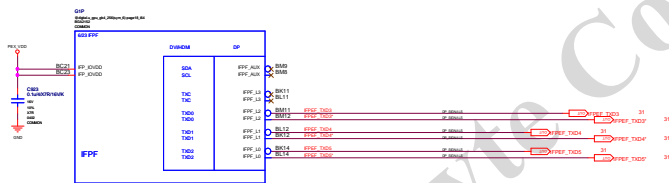
DEL DP Power U4 by xiang.l@20160402



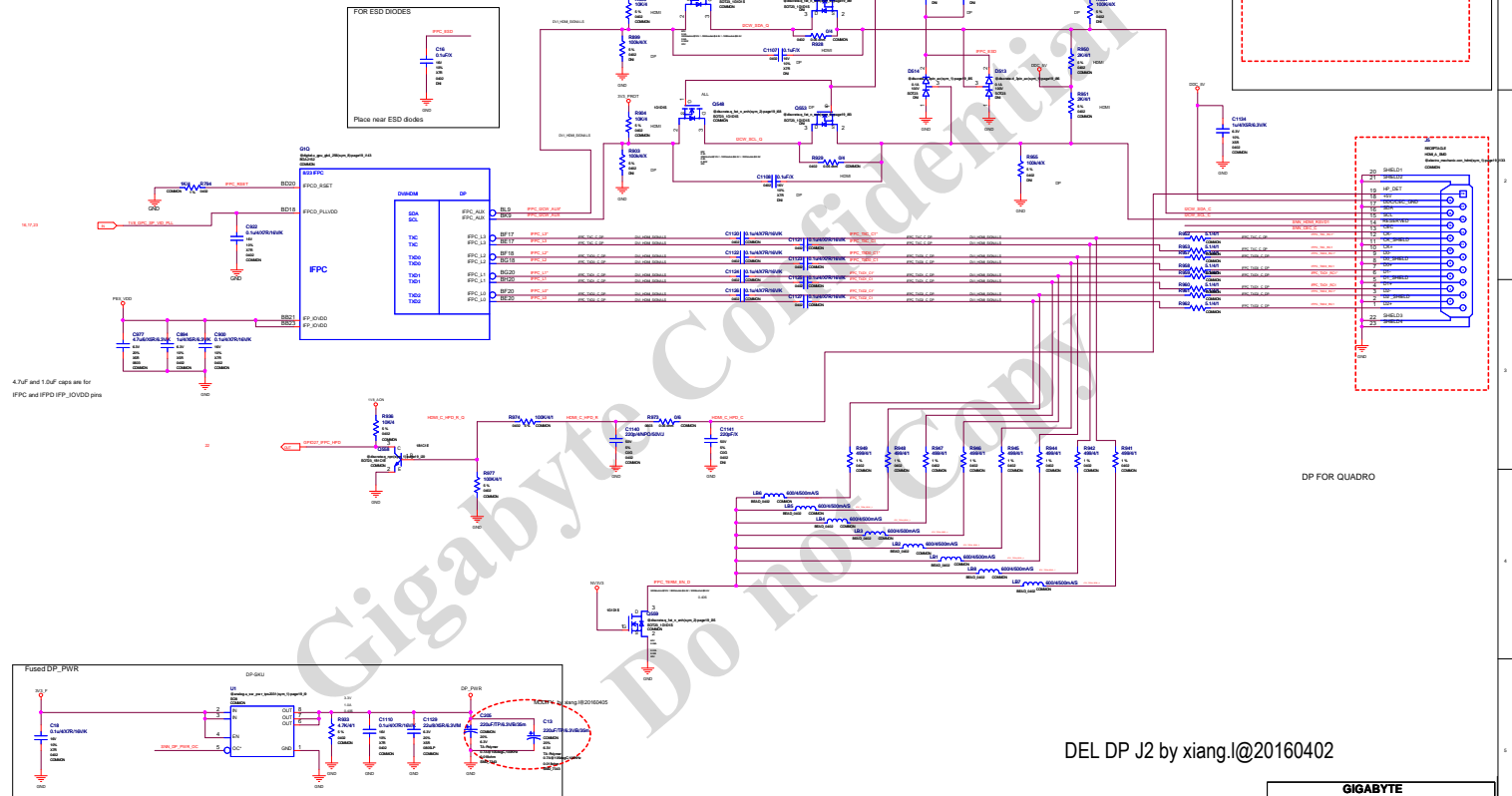
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Two cases to be considered:  
1. DP AUX to DP connector: AUX AC coupled  
2. DP AUX to DP-DVI bridge: AUX pass through

DEL DP J4 by xiang.l@20160402



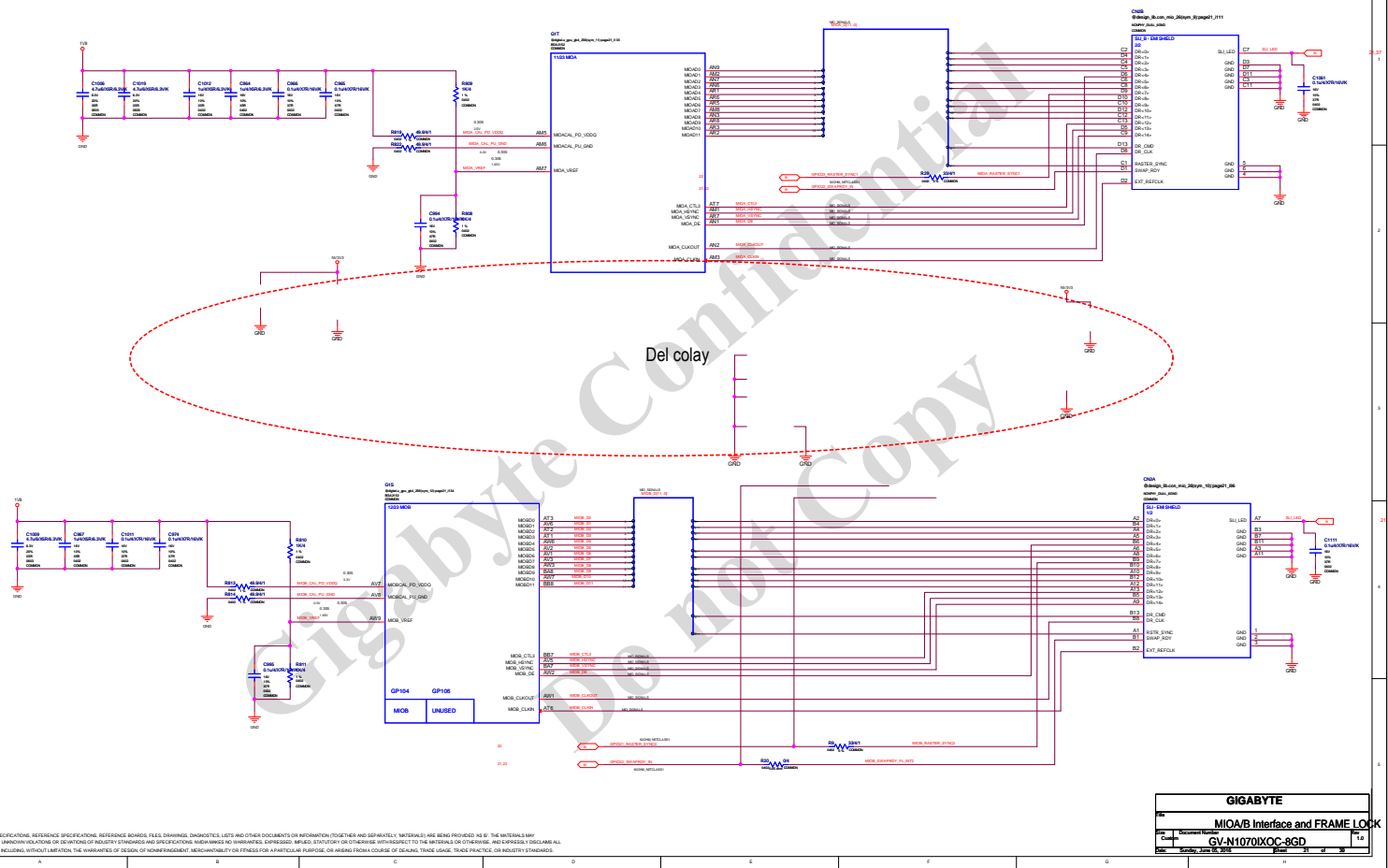
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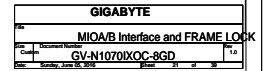
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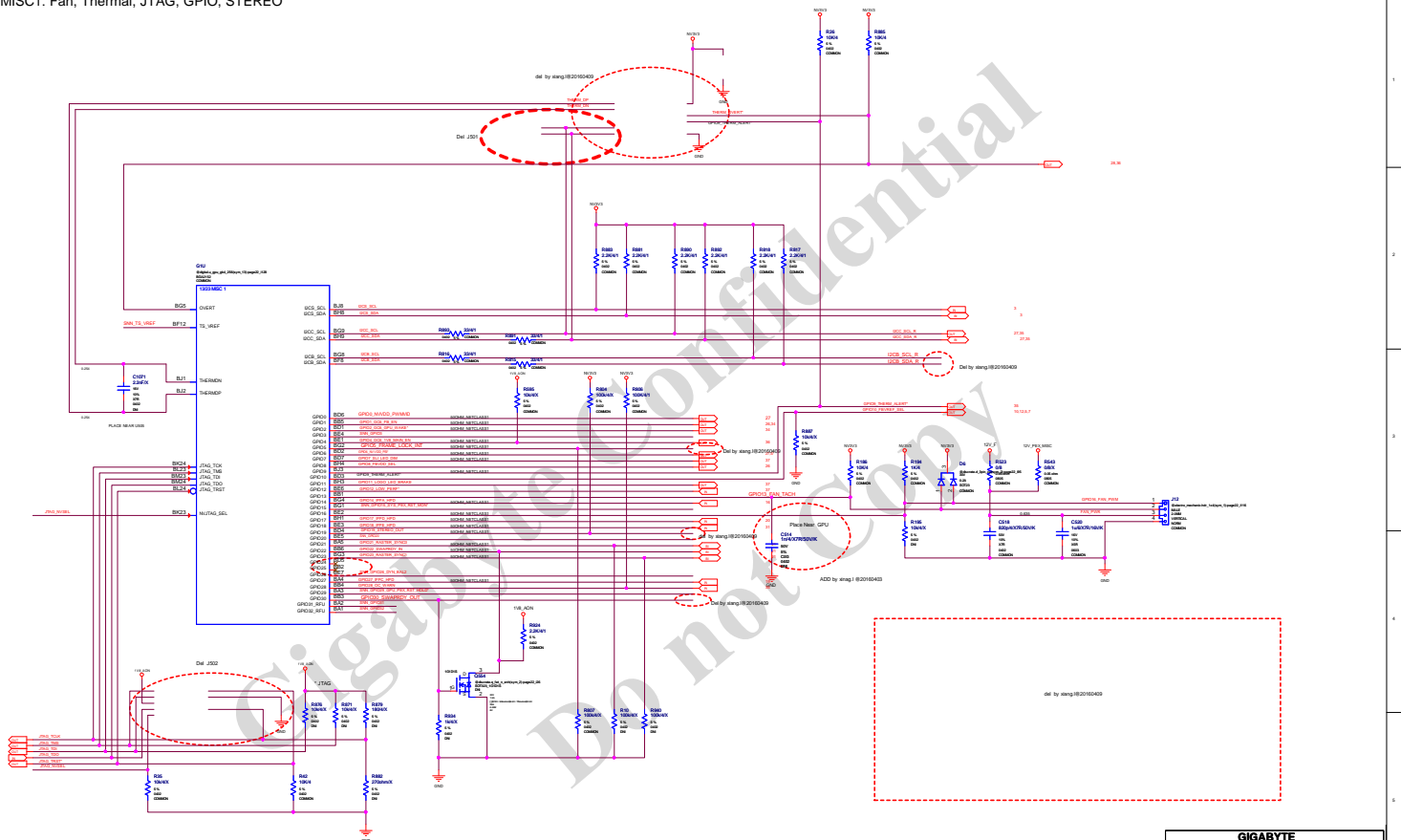
GIGABYTE			
IFPC HDMI 2.0/DP			
GV-N1070IXOC-8GD			
1.0			





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GIGABYTE	
Model	IPPD DP
Part Number	GV-N1070XOC-8GD
Revision	1.0

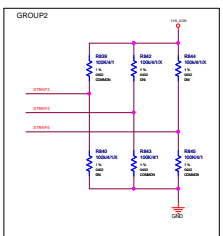
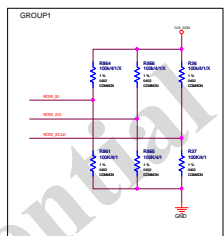
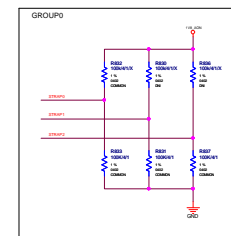
STRAP2	STRAP1	STRAP0	RAMCFG[4:0]		
L	L	L	00000		
L	H	L	00010		
L	H	H	00011		
H	H	L	00110		
H	H	H	00111		
ROM_SO	ROM_SI	ROM_SCLK	SOR_EXPOSED[3:0]	1-ENABLE 0-DISABLE	
L	L	L	1111	DEFAULT	SOR0/1/2/3-ENABLE
L	L	H	1110		
L	H	L	1101		
L	H	H	1100		
H	L	L	1011		
H	L	H	1010		
H	H	L	1001		
H	H	H	1000		
L	L	M	0111		
L	M	L	0110		
L	M	H	0101		
L	H	M	0100		
H	L	M	0011		
H	M	L	0010		
H	M	H	0001		
H	H	M	0000		

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1
L	L	L	0	0	0	0

H-High :Tied to 1.8V  
M-Middle:Tied to 0.9V  
L-Low :Tied to 0V

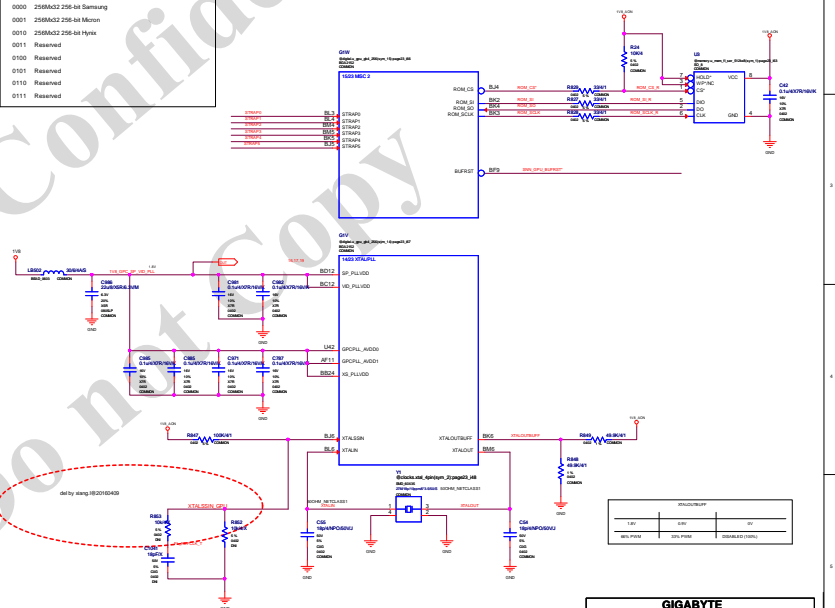
1:SMB\_ALT\_ADDR ENABLE  
0:SMB\_ALT\_ADDR DISABLE  
1:DEVID\_SEL REBRAND  
0:DEVID\_SEL ORIGINAL

1:PCIE\_CFG LOW POWER  
0:PCIE\_CFG HIGH POWER  
1:VGA\_DEVICE ENABLE  
0:VGA\_DEVICE DISABLE



CFGD[5] Config Width Vendor

0000	2558A02 256-bit Samsung
0001	2558A02 256-bit Micron
0010	2558A02 256-bit Hynix
0011	Reserved
0100	Reserved
0101	Reserved
0110	Reserved
0111	Reserved



STRAP5

1:0	1:0	1:0
SEL PWR0	SEL PWR0	DISABLED (INIT)

GIGABYTE

MISC2: ROM, XTAL, Straps

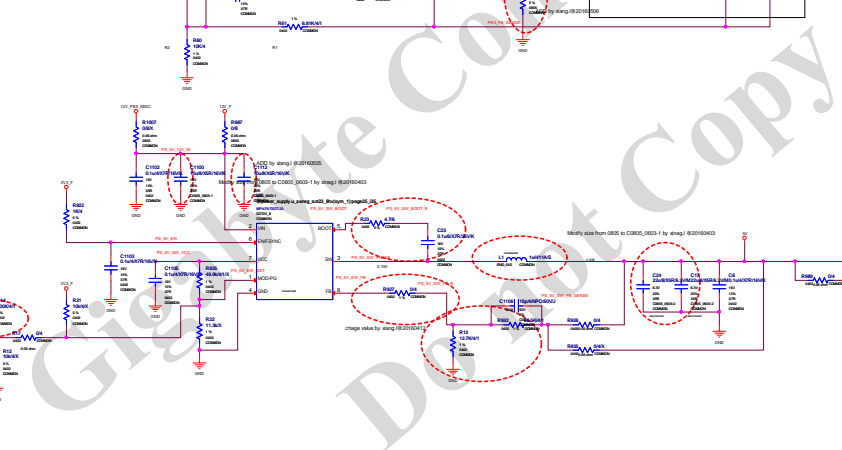
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1.4

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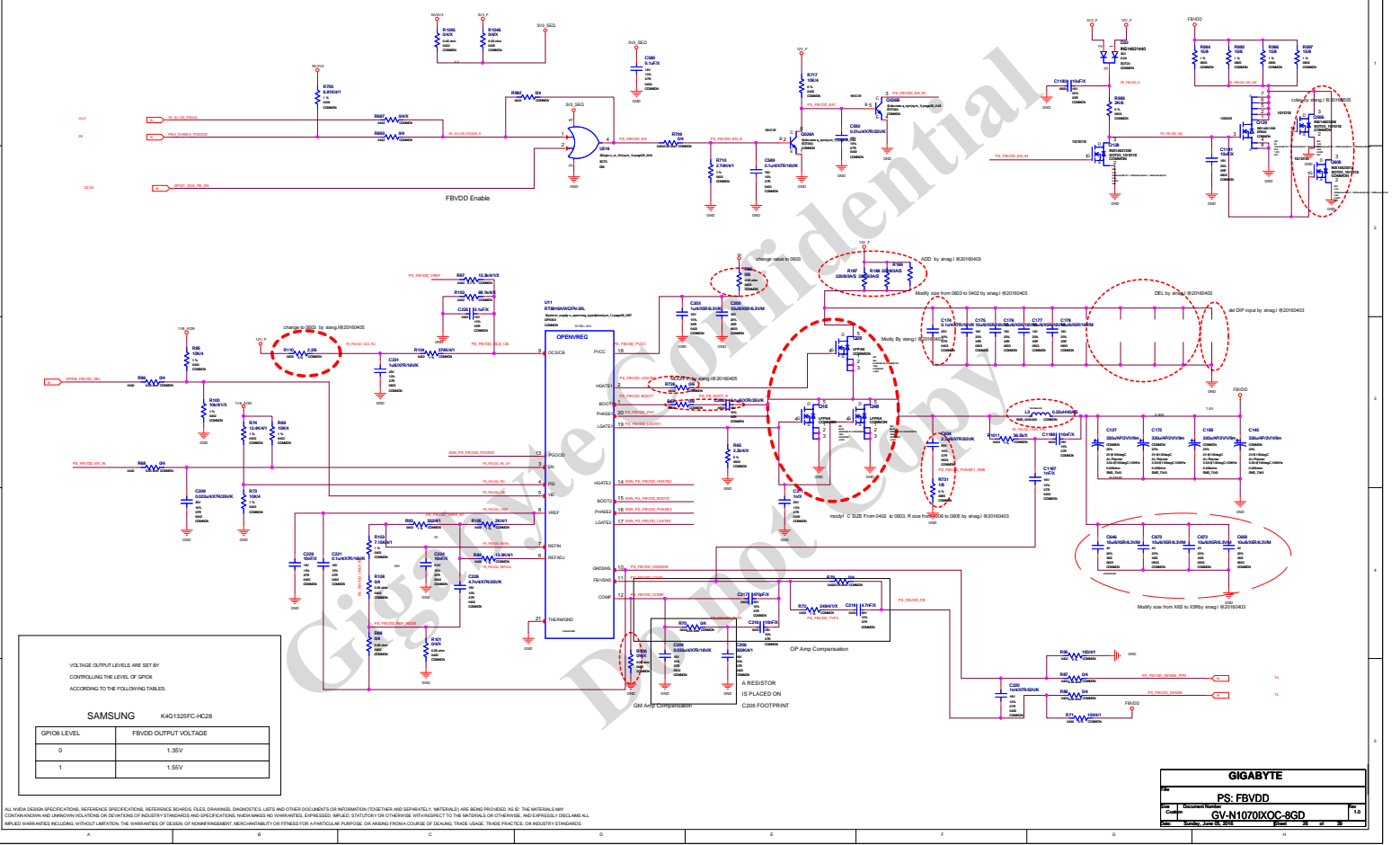


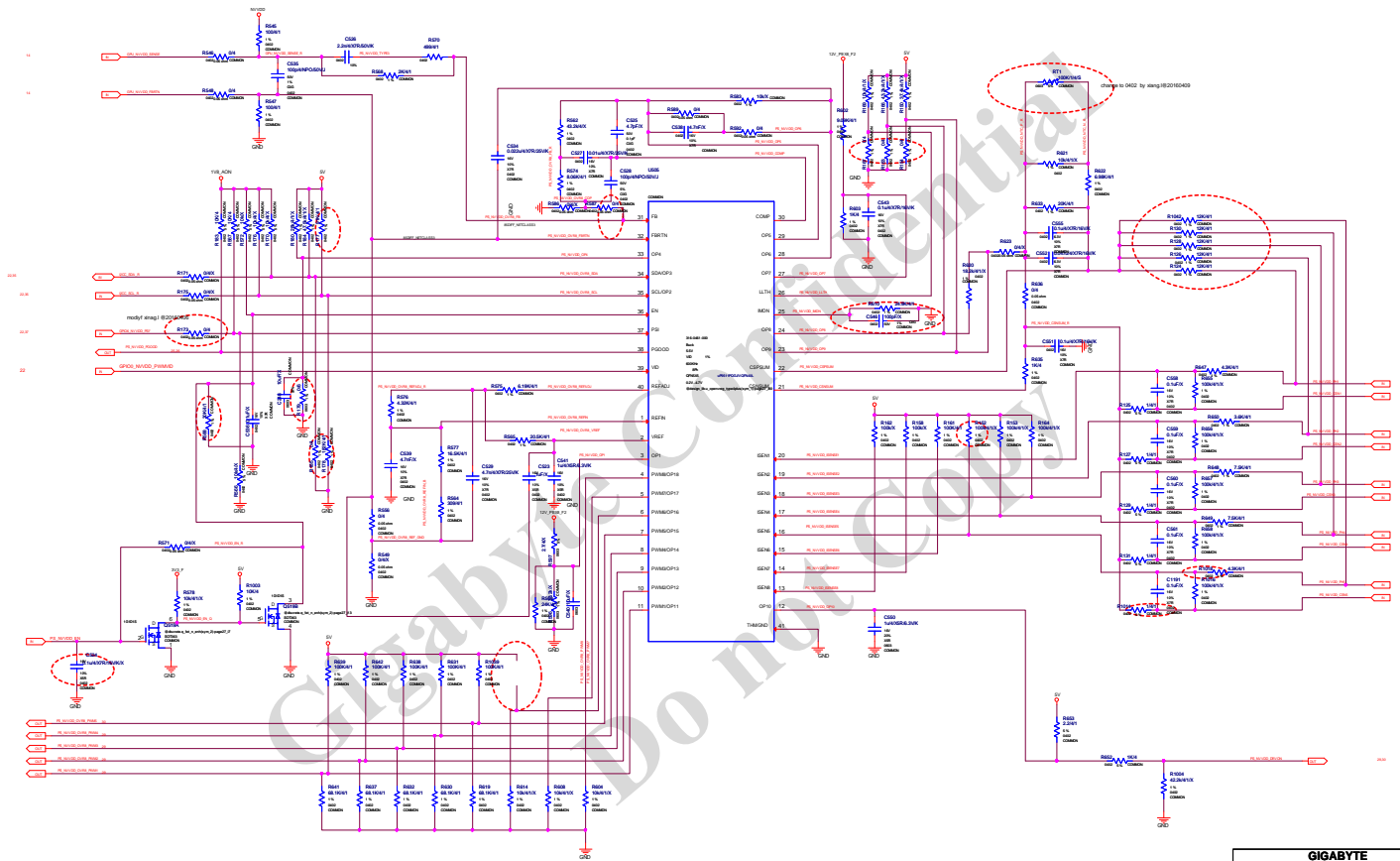



$$4.2675 = 0.8 * (1 + 40.29K / 7.88K)$$

54

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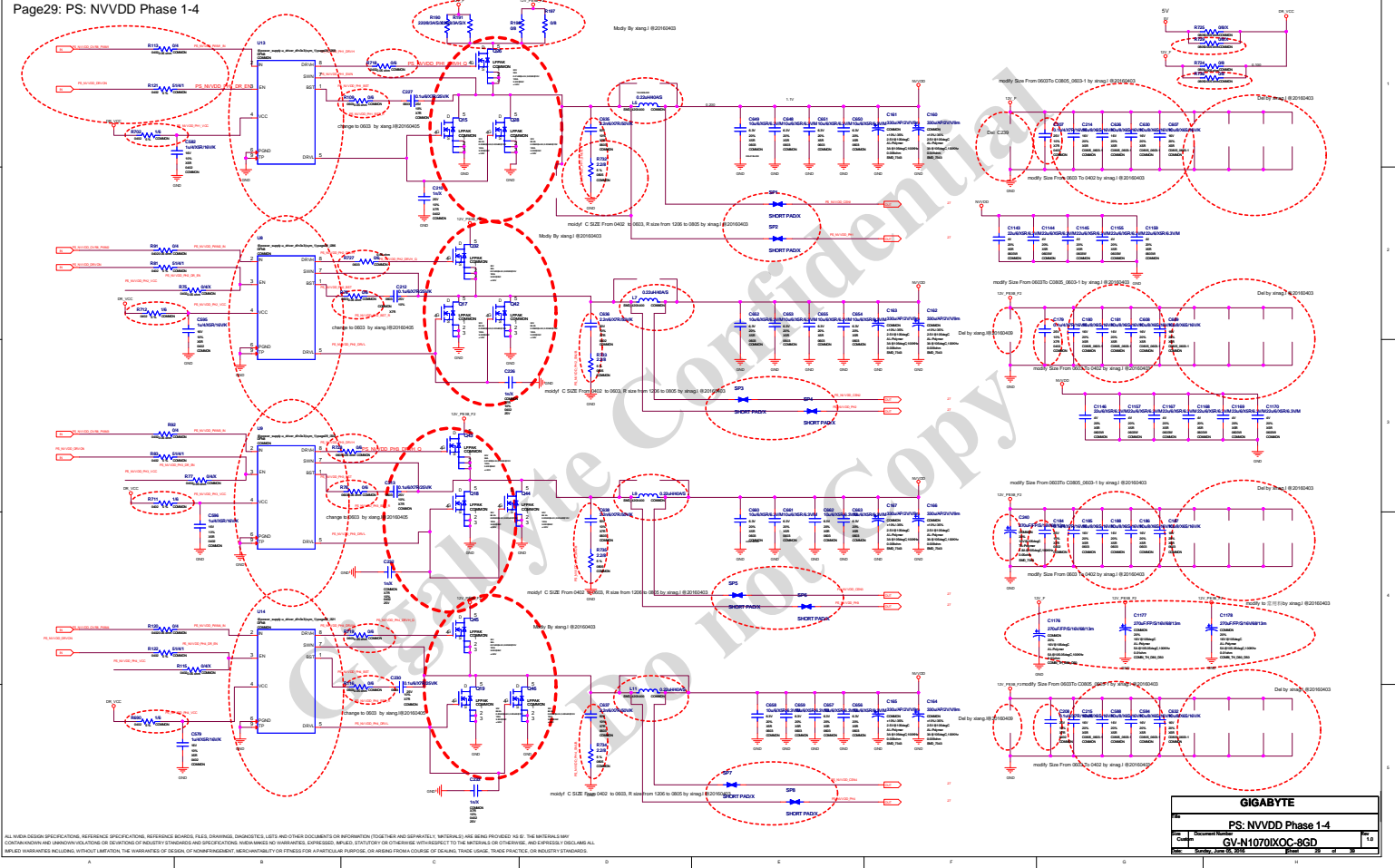




GIGABYTE	
PS: NVVDD_OVR8	
GV-N1070IXOC-8GD	
1.4	

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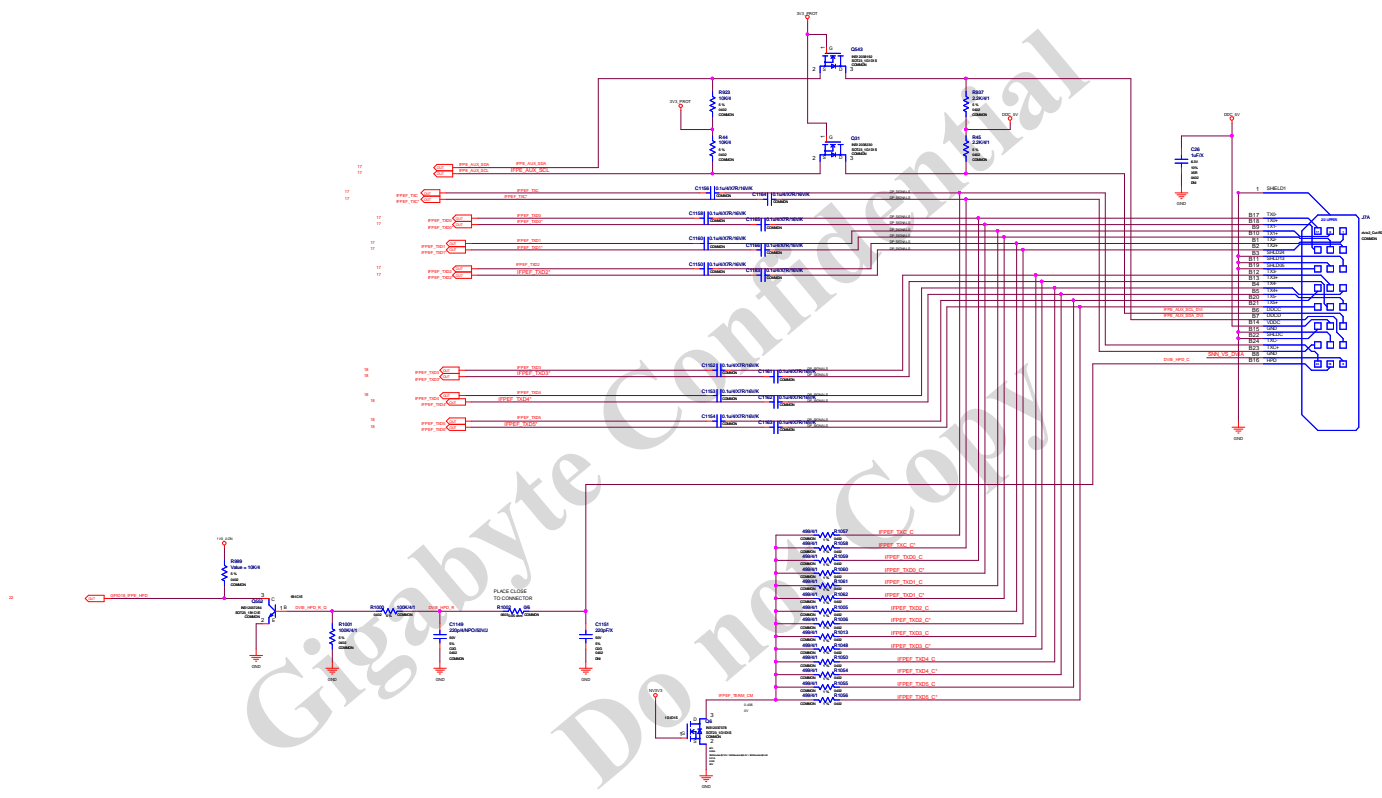




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PS: NVVDD Phase 1-4	
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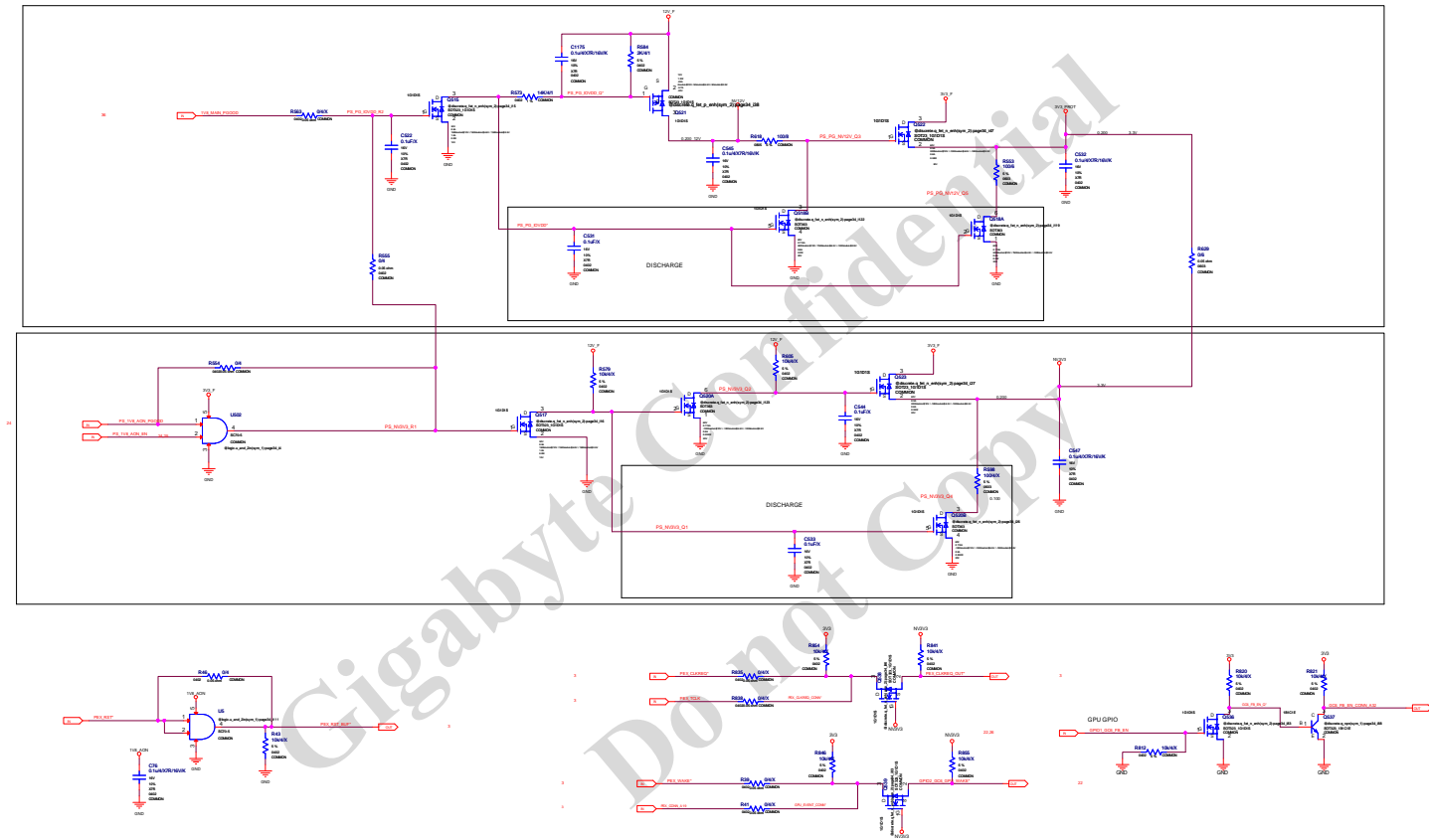
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PS: Dynamic Power Balance Phases	
GV-N1070IXOC-8GD	



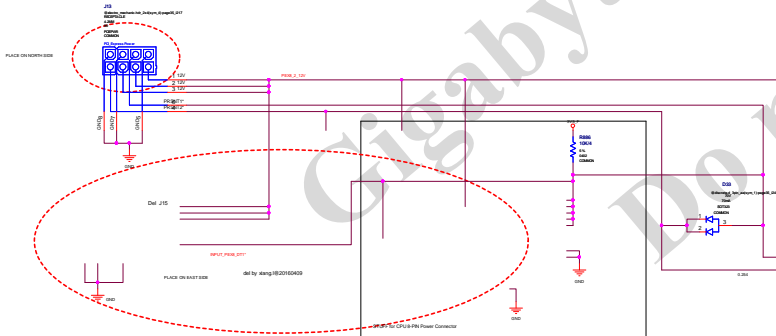
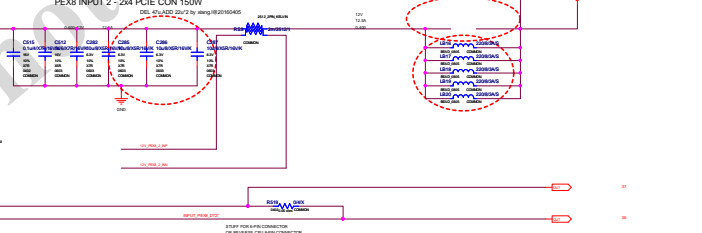
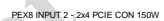
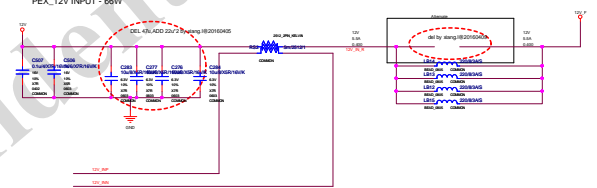
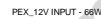
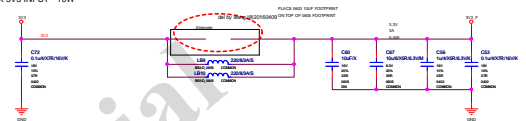
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PS: Dynamic Power Balance Logic	
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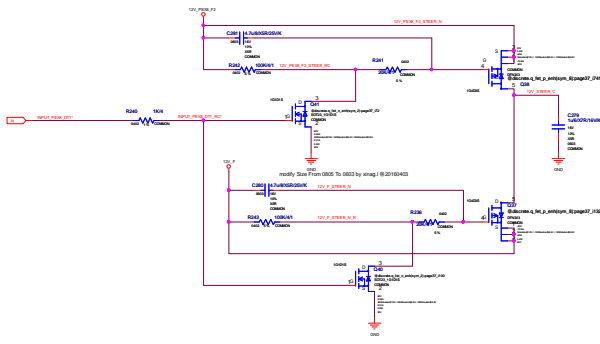
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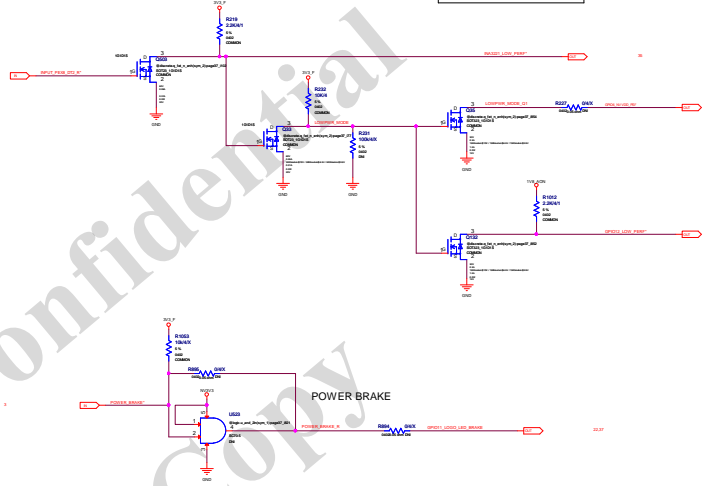
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Date	Sunday, June 06, 2010	Sheet 55 of 58



PEX Input - 12V Current Steering FETs

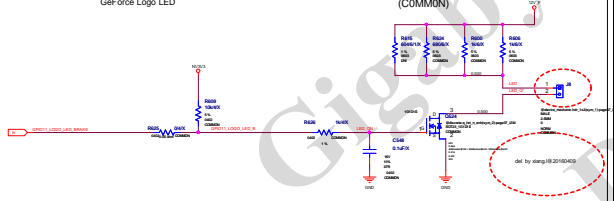


PEX Input - Power Level(PSI)\* Control

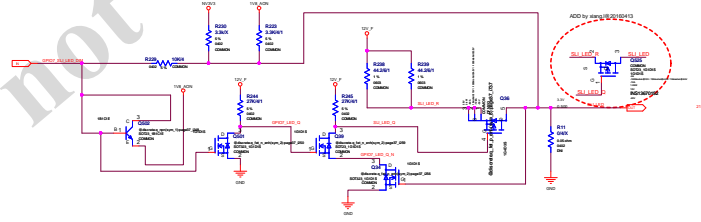


GeForce Logo LED

LED HEADER (COMMON)



SLI LED (GEFORCE ONLY)

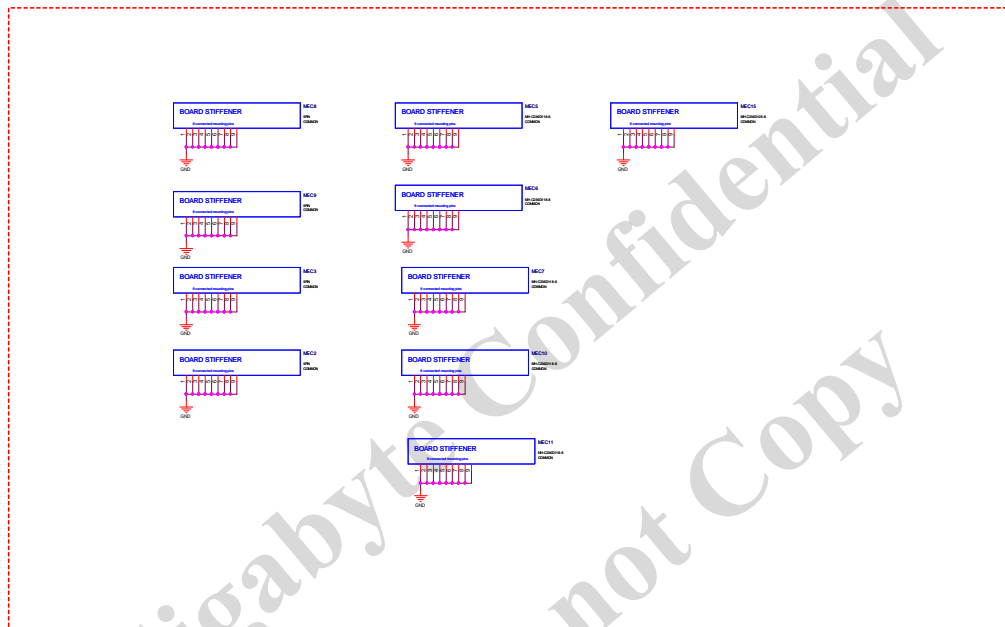


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PS: 12V Current Steering, PSI Control, and LED

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